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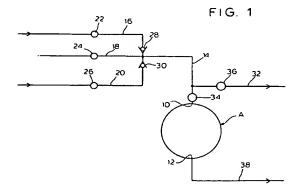
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(54) Gas flow system for CVD reactor.

To minimize contamination of gas flow lines and reactor surfaces from high impurity concentrations present in a CVD reactor (A), control (34) of the dopant gas supply is located closely adjacent to the reactor input port (10) and the dopant gas supply line (14) is separately vented (32). First and second dopant gas supplies (16:20) and a diluent gas supply (18) are connected to branch lines which converge to form the dopant supply line (14). A solenoid valve (34) is situated in the main dopant supply line as close to the input port (10) as possible. A vent line (32) is connected to the dopant supply line (14), prior to the solenoid valve (34). The etchant (C) and silicon (D) gas supplies are each connected to the reactor input by a separate supply line (40:48). The etchant and silicon gas supply lines are vented separately (46:54) from the dopant gas supply line (14).



The present invention relates to chemical vapor deposition (CVD) reactors and more particularly to a gas flow system for a CVD reactor which minimizes the deleterious residual effect of highly concentrated dopant gas in reactor plumbing system and on reactor chamber surface. By minimizing this deleterious residual effect, the impurity concentration of silicon epitaxial layers can he controlled in consecutive production runs.

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Semiconductor devices such as diodes and power transistors are often fabricated on silicon wafers. To form the multiple layers of given resistivity and polarity necessary to obtain the devices, conventional diffusion processes are employed. The silicon wafer is masked and exposed to sources of various impurities level and type at elevated temperature. Thereafter the wafers are diced into individual chips which are connected to terminals and packaged.

The doped layer must be formed in a very precise manner. Otherwise, the physical and electrical properties of the resulting devices will not meet specification. Moreover, the fabrication of quality semiconductor devices must take place at a commercially acceptable yield rate.

Certain silicon wafers for use in making semiconductors are formed by an epitaxial process known as vapor deposition. The silicon layer is grown in a reactor chamber in which precise conditions can be maintained. These chambers are commonly called chemical vapor deposition (CVD) reactors.

In the CVD reactor, supply gases can be utilized in specific amounts and under specified temperatures and pressures to grow silicon layer with precisely controlled properties on a heavily doped silicon substrate. Conventionally, these are single layer epitaxial silicon wafers which must be transferred to a separate chamber called diffusion tube for formation of another doped layer. This is necessary for semiconductor fabrication.

Recently, a new all epitaxial process for growing a multilayer semiconductor structure has been invented. That process is described in our copending application of even date corresponding to U.S. application 015,384 filed 9th February, 1993 and entitled "Multilayer Epitaxial Structure and Method for Fabricating Same" (Lloyd Wise, Tregear & Co. Ref. 37926). As described in detail in that application, the unique structure includes abrupt profile interfaces, a lightly doped layer and a relatively heavily doped, very low ohmic layer, for example with impurity concentrations in the order of 1 x 1019 per cm. The presence of such high impurity concentrations in a CVD reactor normally have adverse affects on subsequent productions runs in the reactor because of the so called residue "memory effect" of the dopant impurities remaining

in the gas flow system and reactor chamber surfaces. These residual impurities may prevent precise resistivity control in subsequent processing in the chamber.

There will be described below a gas flow system embodying this invention for use with a CVD reactor which is employed in an epitaxial process where relatively heavy impurity concentrations of dopant impurities are present. The system significantly reduces the residual memory effect of the heavy impurity concentrations, leading to more precise resistivity control in subsequent production runs and thus higher quality wafers, resulting in higher device yields.

This objective is accomplished by modifying conventional CVD reactor gas flow systems to permit control of the supply of dopant source gases at a location in the main dopant gas supply line adjacent to the reactor input port as opposed to conventional CVD system where the gas mixing occurs in the gas control panel. In addition, the dopant gas supply line is separately vented from the other gas supply lines. Flow control devices, such as valves or check valves, etc., are located in the flow lines at places where gases might otherwise mix. Exhaust scrubber conditions are maintained such that adequate pressure conditions exist on all vent, purge and exhaust lines from the reactor in order to minimize or prevent back pressures and contaminations.

The practice of the present invention enables us to: provide a gas flow system for a CVD reactor which significantly reduces or eliminates the residual memory effect on subsequent production runs due to the presence of heavy impurity concentrations:

to provide a gas flow system for a CVD reactor in which the supply of dopant gases is controlled at a point proximate the reactor input port,

to provide a dopant gas supply system for a CVD reactor which is separately vented from the other gas supply lines.

In accordance with one aspect of the present invention, a gas flow system is provided for a chemical vapor deposition reactor of the type used in growing epitaxial structures having multilayers of high impurity concentrations as well as light impurity concentration. The reactor has a chamber with an input port for receiving gases from first and second gas supplies and an exhaust port. The system comprises first and second gas supply lines connected to said first and second gas supplies, respectively. A vent line is provided. First gas supply line valve means are located in first gas supply line, adjacent to the input port. The vent line is operably connected to the first gas supply line, prior to the valve means. An exhaust line is connected to the output port.

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The system also includes check valve means in the first supply line in a preferred embodiment.

Preferably, the first supply line comprises first and second sections. The first gas supply comprises a source of a first dopant gas and a source of a second dopant gas. The first section is connected to the first dopant gas source. The second section is connected to the second gas source.

First section valve means are located in the first section. Second section valve means are located in the second section.

The system also includes first and second check valve means in the first and second sections, respectively. The second gas source comprises a source of diluent gas.

The first supply line valve means comprises a solenoid valve. The first and second section valve means are located in the second section.

The system also includes first and second check valve means in the first and second sections, respectively. The second gas source comprises a source of diluent gas.

The first supply line valve means comprises a solenoid valve. The first and second section valve means also comprise solenoid valves.

In accordance with another aspect of the present invention, a gas flow system is provided for a chemical vapor deposition reactor of the type used in growing epitaxial structures having multilayers of high impurity concentrations, as well as light impurity concentrations. The reactor has a reactor chamber with a gas input port adapted to receive input gases from first, second and third input gas supplies, respectively, and an output port. The system includes first, second and third branch lines operably connected to the first, second and third gas sources respectively. Each of the first, second and third branch lines also include a gas vent section. The gas vent section of the first branch is separate from the other vent sections. The gas input sections are operably connected to the chamber input port. An exhaust line is operably connected to the chamber output port.

Preferably the first branch line comprises valve means. The valve means is situated adjacent to the input fort.

The first input gas supply may be, for example, a source of dopant, the second input gas supply, a source of etchant and the third input gas supply may be a source of silicon. The first input gas supply preferably includes a first source of dopant gas and a second source of dopant gas. The first branch line preferably includes a first gas source section and a second gas source section. Each of the first and second gas source sections comprise valve means.

A gas flow system for a CVD reactor, embodying the invention will now be described with refer-

ence to the accompanying drawings, wherein like numerals refer to like parts in which:

Fig. 1 is a schematic diagram illustrating the dopant supply control aspect of the present invention; and

Fig.2 is a schematic diagram illustrating the separate venting aspect of the present inventions.

As seen in Fig. 1, the CVD reactor chamber, generally designated A, is provided with an input port 10 and an exhaust port 12, as is conventional. Input port 10 is connected to the dopant supply portion of the gas flow system which includes a main dopant gas supply line 14. Main supply line 14 in turn is fed by three branch supply lines 16, 18, 20. Branch lines 16 and 20, in the example are each connected to a different source of an impurity dopant gas, respectively, such as sources of gases containing boron or phosphorous. Branch line 18 is connected to receive a diluent gas, such as hydrogen, which may be mixed with the dopant gases prior to entering Chamber A or may be used as a purge.

Each branch supply line 16, 18 and 20 contains a solenoid valve 22, 24, and 26 respectively, which controls gas flow therein by opening and closing in response to the application of the appropriate electric signal. Further, check valves 28, 30 and situated in lines 16 and 20, respectively. The check valves are located proximate the point where the branch supply lines converge to form main dopant supply gas line 14.

Main dopant gas supply line 14 is connected to a vent line 32 which serves to vent all of the dopant gas supply lines. Vent line 32 is connected to main supply line 14 close to a solenoid valve 34 which controls the flow from the main supply line into input port 10. Valve 34 is located in main supply line 14 adjacent to input port 10. A solenoid valve 36 is also present in vent line 32. This configuration permits the entire supply line system to be thoroughly purged when required.

The exhaust portion of the gas flow system includes an exhaust line 38. Exhaust line 38 is connected to reactor output port 12.

Fig. 2 illustrates the overall gas flow system. The dopant supply portion, generally designated B, is preferably as structured as illustrated in Fig. 1. However, for simplicity, the dopant gas supply is here illustrated as a single supply line 16. The etchant gas supply portion is generally designated C. The silicon source gas supply portion is generally designated D.

Etchant supply portion C includes a supply line 40 with a solenoid valve 42. Supply line 40 splits into a feed line 44 and a vent line 46. Similarly, silicon source gas supply portion D includes a supply line 43 with a solenoid valve 50. Line 48 splits into a feed line 52 and a vent line 54. Feed

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lines 44 and 52 are connected to the input port 10 of the reactor. Vent lines 46, 54 may be connected for form a common vent line 56. However, vent line 56 is separate from the dopant gas supply vent line

Solenoid valves 58 and 60 are situated in lines 44 and 46 respectively. Solenoid valves 62 and 64 are situated in lines 52 and 54 respectively. Flow rate control devices 66, 68 and 70 of conventional design are situated in lines 16, 42 and 50 of each gas supply portion B, C and D, respectively, such that the rate of gas flow through each portion can be regulated precisely.

It will now be appreciated that there has heed described a gas flow system for a CVD reactor in which the control valve for the dopant gas supply is located adjacent to the reactor chamber input port. Moreover, a separate vent line is provided for the dopant supply, the other gas supply lines having a different vent. Valves are strategically located to prevent gases from mixing. By appropriately pressurizing the system, this configuration minimizes back pressure and contaminations.

While only one preferred embodiment of each aspect of the present invention have been disclosed for purposes of illustration, many variations and modifications could be made thereto. It is intended to cover all of these modifications and variations which fall within the scope of the invention, as defined by the following claims:

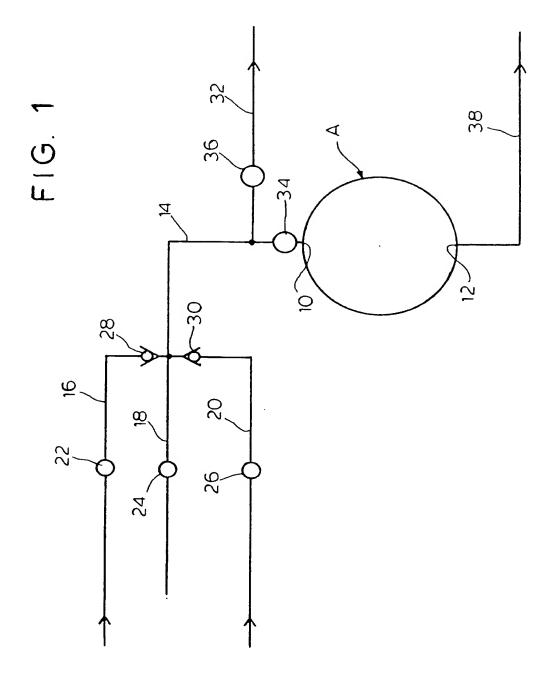
Claims

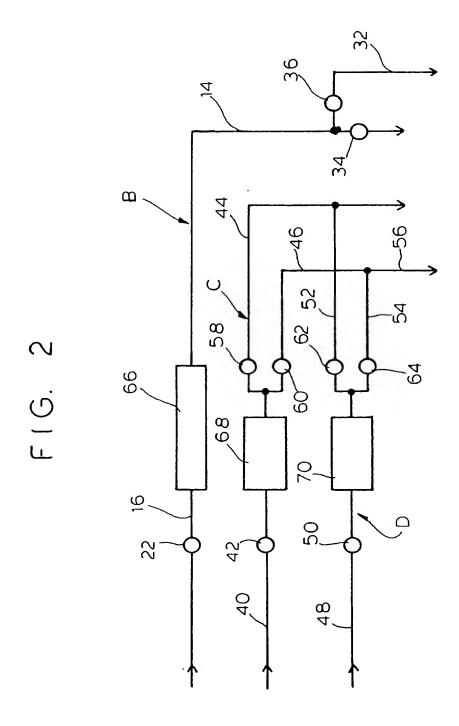
- 1. A gas flow system for a chemical vapor deposition reactor of the type adapted for use in growing epitaxial structures, the reactor having a chamber with an input port for receiving gases from first and second gas supplies and an exhaust port, the system comprising first and second gas supply lines connecting the first and second gas supplies, respectively, to said input port, a vent line, valve means located in ,said first gas supply line adjacent to said input port, said vent line being operably connected to said first gas supply line prior to said valve means, and an exhaust line operably connected to the output port.
- 2. The system of Claim 1 further comprising second valve means in said second gas line.
- 3. The system of Claim 1 wherein said first gas supply line comprises first and second sections and wherein the first gas source comprises a source of a first dopant gas and a source of a second dopant gas, said first section being connected to said first dopant gas source and said second section having con-

nected to said second gas source.

- 4. The system of Claim 3 wherein said first section comprises first section valve means and said second section comprises second section valve means.
- 5. The system of Claim 3 wherein said first gas supply further comprises a source of diluent gas further comprises a third section connected to said source of diluent gas.
- 6. The system of Claim 3 further comprising first and second check valves in said first and second section.
- 7. The system of Claim 6 wherein said first and second check valves are situated in said first and second sections proximate said first gas supply line.
- 8. The system of Claim 1 wherein said first gas supply line valve means comprises a solenoid valve.
- 9. The system of Claim 3 wherein said first and second sections comprise solenoid valves.
- 10. A gas flow system for a chemical vapor deposition reactor of the type adapted for use in growing epitaxial structures, the reactor having a reaction chamber with input port means adapted to receive gases from first, second and third input gas supplies, respectively, and an output port, the system comprising first, second and third branch lines operably connecting said first, second and third gas supplies, respectively to said input port means, each of said first, second and third branch lines respectively comprising a gas vent section, said gas vent section of first branch line being separate from said gas vent sections of said second and third branch lines, and an exhaust line operably connected to the chamber output means.
- 11. The system of Claim 10 further comprising valve means in said first branch line located as close as possible to said input port means.
- 12. The system of Claim 10 wherein said first input gas supply is a source of dopant gas.
- 13. The system of claim 10 wherein said second input gas supply is a source of etchant gas.
- 14. The system of Claim 10 wherein said third input gas supply is a source of silicon.

15. The system of Claim 10 wherein said first input gas supply comprises a source of a first dopant gas and a source of a second dopant gas and wherein said first branch line comprises a first gas source section and a second gas source section, each of said first and second gas source sections comprising separate valve means.





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EUROPEAN SEARCH REPORT

Application Number EP 94 30 1700

A PT/ELEKTROTECHNIEK ELEKTRONICA, OCT. 1988, NETHERLANDS, VOL. 43, NR. 10, PAGE(S) 48 - 54, ISSN 0032-4086 van Sark W 'Study of high performance solar cell fabrication with UNIX and VME-bus process control' * figure 1 * A PATENT ABSTRACTS OF JAPAN vol. 013, no. 119 (C-579) 23 March 1989 & JP-A-63 291 895 (SUMITOMO METAL IND LTD) 29 November 1988 * abstract * The present search report has been drawn up for all claims	Category	Citation of document with indication, of relevant passages	where appropriate,	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CL6)
Vol. 004, no. 048 (E-006) 12 April 1980 & JP-A-55 018 024 (CHIYOU LSI GIJUTSU KENKYU KUMIAI) 7 February 1980 * abstract * JOURNAL OF THE ELECTROCHEMICAL SOCIETY, MARCH 1985, USA, VOL. 132, NR. 3, PAGE(S) 662 - 668, ISSN 0013-4651 Tandon J L et al 'Large-scale growth of GaAs epitaxial layers by metal organic chemical vapor deposition' *EXPERIMENTAL METHODS* A US-A-4 917 136 (OHMI T) 17 April 1990 * column 10, line 21 - line 29 * EXCHINICAL PRELIDS SEABCHEED (Med. C) TECHNICAL PRELIDS SEABCHEED (Med. C) TECHNICAL PRELIDS C C30C C30B TECHNICAL PRELIDS C C30C C30B TECHNICAL PRELIDS C C30C C30B TECHNICAL PRELIDS SEABCHEED (Med. C) TECHNICAL PRELIDS SEABCHEED (Med	X	1985	ET AL.) 6 August	1-7,10	
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* column 10, line 21 - line 29 * PT/ELEKTROTECHNIEK ELEKTRONICA, OCT. 1988, NETHERLANDS, VOL. 43, NR. 10, PAGE(S) 48 - 54, ISSN 0032-4086 van Sark W 'Study of high performance solar cell fabrication with UNIX and VME-bus process control' * figure 1 * PATENT ABSTRACTS OF JAPAN vol. 013, no. 119 (C-579) 23 March 1989 & JP-A-63 291 895 (SUMITOMO METAL IND LTD) 29 November 1988 * abstract *	A	MARCH 1985, USA, VOL. 132 662 - 668, ISSN 0013-4653 Tandon J L et al 'Large- GaAs epitaxial layers by chemical vapor deposition	2, NR. 3, PAGE(S) scale growth of metal organic	1-15	
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vol. 013, no. 119 (C-579) 23 March 1989 & JP-A-63 291 895 (SUMITOMO METAL IND LTD) 29 November 1988 * abstract * The present search report has been drawn up for all claims	A	NETHERLANDS, VOL. 43, NR. 10, PAGE(S) 48 - 54, ISSN 0032-4086 van Sark W 'Study of high performance solar cell fabrication with UNIX and VME-bus process control'		1-15	
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